

NPO-P0001E-US

## COMBINED DECLARATION AND POWER OF ATTORNEY

As a below named inventor, I hereby declare that:

I believe I am the sole (if only one name appears below), or joint (if more than one name appears), original and first inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled:

"Integrated level shift and override circuit for a processor system"	
+ The specification for the ab	ove entitled invention is filed herewith.
The specification for the ab	ove entitled invention was filed previously
with application serial number:	Filing Date:
I hereby state that I have review	wed and understand the contents of the above identified mended by any amendment referred to above.
the invention disclosed in this applic Regulations, Section 1.56 (a). I furth application to disclose to the Patent and to the patentability of the invention d	iose information which is material to the patentability of cation in accordance with Title 37, Code of Federal her acknowledge the duty in any continuation-in-part 1 Trademark Office all information known to be material lisclosed in this application, as defined in 1.56, which ing date of the prior application and the filing date of this
<u>P1</u>	RIORTY CLAIM
There is no claim of priority	
Claim of priority is based on Provisional Application	the following: No. = 6%255, 6/8 2/13
Filing Date = 2000/1:	2/13
V	ER OF ATTORNEY
•	by appoint the following attorney to prosecute this usiness in the Patent and Trademark Office:
Winston Hsu, Registrat 5F, No.389, Fu-Ho Rd Taipei Hsien, Taiwan, TEL: +886-2-8923-735	l., YUNGHO City, R.O.C.

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I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment or both under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued hereon.

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